

Patent claims:

1. A method for operating a transmitting device (1) of an access system with a plurality of longwave antennas (LF_{1-n}), in which the longwave antennas (LF_{1-n}) are jointly triggered by a central power amplifier (2) and are individually activated by a multiplexer device (4), **characterized in that** the transmitter current (I_{LF}) is regulated.
2. A method according to claim 1, in which the actual value of the transmitter current (I_{LF}) is detected and if a desired value (I_{Ref}) is exceeded, the transmitter current (I_{LF}) is approximated to the desired value (I_{Ref}) by pulse-width modulation of the input signal (P_{in}) of the amplifier device (2).
3. A method according to one of claims 1 to 2, in which by means of the power amplifier (2) a square wave or trapezoidal output voltage (LF_{out}) is generated for triggering the longwave antennas (LF_{1-n}).
4. A transmitting device (1) for an access system, in particular of a motor vehicle

 - with a plurality of longwave antennas (LF_{1-n})
 - with a multiplexer device (4) for activating one longwave antenna (LF_n) each and
 - with a joint amplifier device (2), at the outlet (LF_{out}) thereof the longwave antennas (LF_{1-n}) are jointly connected, **characterized in that**
 - a control unit (10) is provided for regulating the transmitter current (I_{LF}).

5. A transmitting device according to claim 4, in which a device (8) for detecting the actual value of the transmitter current (I_{LF}) and a control unit (10, 12, 14) for pulse-width modulation of the input signal (P_{in}) of the amplifier device (2) is provided, wherein the control unit initiates the transmitter current (I_{LF}) to approximate the desired value (I_{Ref}), if the desired value (I_{Ref}) is exceeded.
6. A transmitting device according to claim 5, with a control unit (10) connected upstream to the amplifier device (2) on the input side and downstream to the multiplexer unit 4, for limiting the transmitter current (I_{LF}).
7. A transmitting device according to one of claims 6, with a control device (14), which is connected on the output side to a control input (P_{in}) of the amplifier device (2), which control device (14) comprises a first input (E_1) for a clock signal (LF_{clk}) and second input (E_2) for a control signal (S_T).
8. A transmitting device according to claim 7, in which the control device (14) comprises a logical combination element (16) with a first input for the clock signal (LF_{clk}) and with a second input, to which the comparator (12) is connected on the output side via a sequential circuit (18), wherein as a sequential circuit (18) a controlled latch-flipflop for pulse-width modulation of a control input signal (P_{in}) of the amplifier device (2) is provided.